

# **DRD710T14**

## **Rectifier Diode**

DS6229 - 1 February 2018 (LN35174)

Replaces DS4794-5.1 June 2002 - Datasheet DS502ST

## **FEATURES**

- Double Side Cooling
- High Surge Capability

## **KEY PARAMETERS**

$V_{RRM}$	1400V
I <sub>F(AV)</sub>	866A
I <sub>FSM</sub>	A000A

## **VOLTAGE RATINGS**

Part and Ordering Number	Repetitive Peak Voltages V <sub>RRM</sub> V	Conditions
DRD710T14 DRD710T12 DRD710T10	1400 1200 1000	$V_{RSM} = V_{RRM} + 100V$

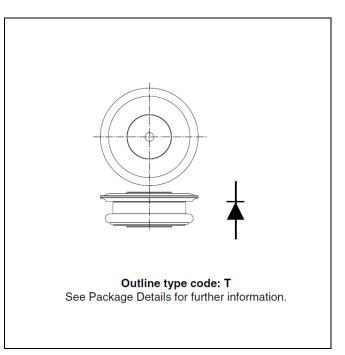


Fig. 1 Package outline

## **ORDERING INFORMATION**

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

**DRD710T12** for a 1200V device



## **CURRENT RATINGS**

## $T_{case} = 75$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Double Si	Double Side Cooled					
I <sub>F(AV)</sub>	Mean forward current	Half wave resistive load	866	А		
I <sub>F(RMS)</sub>	RMS value	-	1360	А		
I <sub>F</sub>	Continuous (direct) on-state current	-	1236	Α		
Single Sid	Single Side Cooled (Anode side)					
I <sub>F(AV)</sub>	Mean forward current	Half wave resistive load	556	Α		
I <sub>F(RMS)</sub>	RMS value	-	873	Α		
I <sub>F</sub>	Continuous (direct) on-state current	-	721	Α		

## $T_{\text{case}}$ = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Double Si	Double Side Cooled					
$I_{F(AV)}$	Mean forward current	Half wave resistive load	710	А		
I <sub>F(RMS)</sub>	RMS value	-	1115	Α		
I <sub>F</sub>	Continuous (direct) on-state current	-	994	Α		
Single Side Cooled (Anode side)						
I <sub>F(AV)</sub>	Mean forward current	Half wave resistive load	449	А		
I <sub>F(RMS)</sub>	RMS value	-	705	А		
I <sub>F</sub>	Continuous (direct) on-state current	-	569	А		



# **SURGE RATINGS**

Symbol	Parameter	Test Conditions	Max.	Units
I <sub>FSM</sub>	Surge (non-repetitive) on-state current	10ms half sine, T <sub>case</sub> = 1750°C	6.5	kA
l <sup>2</sup> t	I <sup>2</sup> t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	211 x 10 <sup>3</sup>	MA <sup>2</sup> s
I <sub>FSM</sub>	Surge (non-repetitive) on-state current	10ms half sine, T <sub>case</sub> = 175°C	8.0	kA
l <sup>2</sup> t	I <sup>2</sup> t for fusing	$V_R = 0$	0.32	MA <sup>2</sup> s

## THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance – junction to case	Double side cooled	DC	-	0.07	°C/W
		Single side cooled	Anode DC	-	0.14	°C/W
			Cathode DC	-	0.14	°C/W
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink	Clamping force 43kN	Double side	-	0.02	°C/W
		(with mounting compound)	Single side	-	0.04	°C/W
T <sub>vj</sub>	Virtual junction temperature	On-state (conducting)		-	185	°C
		Reverse (blocking)		-	175	°C
T <sub>stg</sub>	Storage temperature range			-55	200	°C
F <sub>m</sub>	Clamping force			3.5	5.0	kN

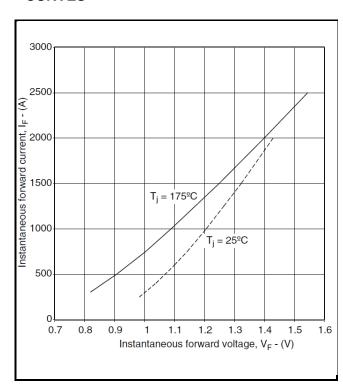
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## **CHARACTERISTICS**

Symbol	Parameter	Test Conditions	Min.	Max.	Units
$V_{FM}$	Forward voltage	At 600A peak, T <sub>case</sub> = 25°C	-	1.2	V
I <sub>RM</sub>	Peak reverse current	At V <sub>RRM</sub> , T <sub>case</sub> = 175°C	-	30	mA
$V_{TO}$	Threshold voltage	At T <sub>vj</sub> = 175°C	-	0.76	V
r <sub>T</sub>	Slope resistance	At T <sub>vj</sub> = 175°C	-	0.32	mΩ

## **CURVES**



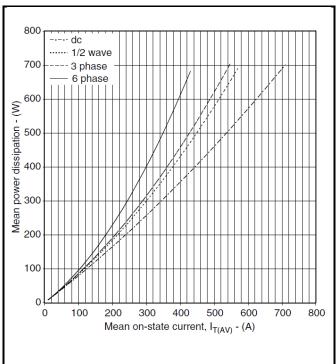


Fig.2 Maximum & minimum on-state characteristics

Fig.3 Dissipation curves

 $V_{\text{TM}}$  EQUATION

 $V_{TM} = A + BIn (I_T) + C.I_T + D.\sqrt{I_T}$ 

Where A = 0.137416

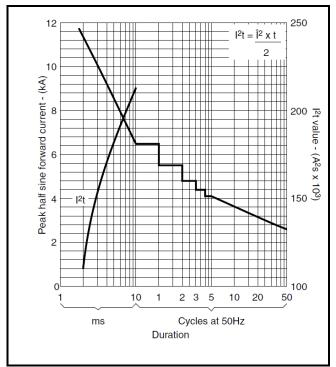
B = 0.109992

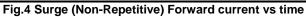
C = 0.000248

D =- 0.00172841

these values are valid for  $T_j = 175$ °C for  $I_F 500A$  to 2500A







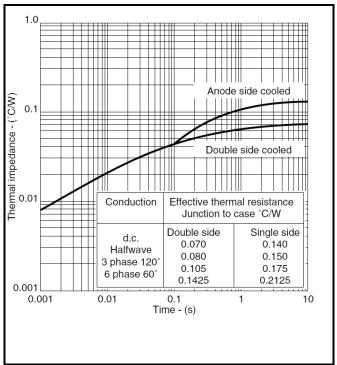
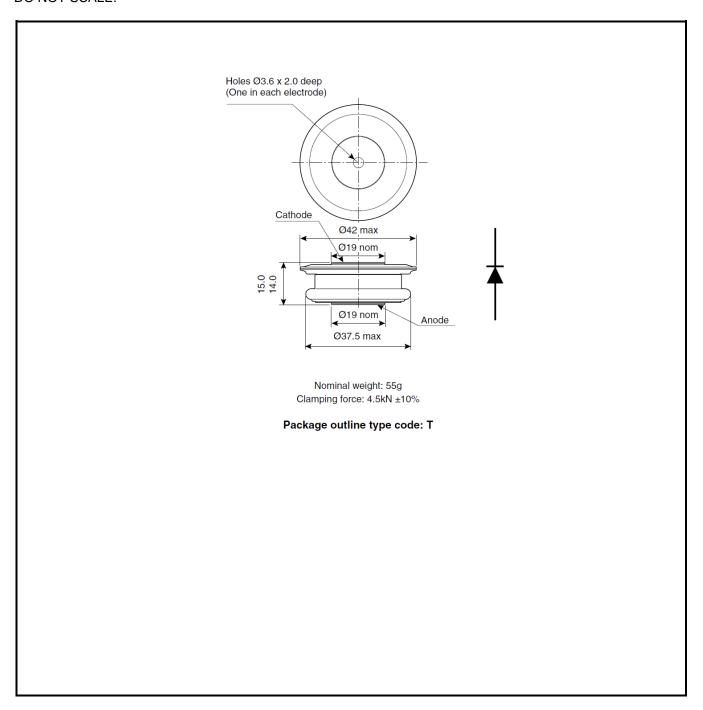


Fig.5 Maximum (limit) transient thermal impedancejunction to case



## **PACKAGE DETAILS**

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



## Note:

Some packages may be supplied with gate and or tags.



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